

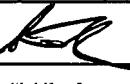
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/085,755
(Use as many sheets as necessary)				Filing Date	May 27, 1998
				First Named Inventor	Frampton E. ELLIS, III
				Art Unit	2153
				Examiner Name	Sean REILLY
Sheet	1	of	3	Attorney Docket Number	313449-P0005

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
SC		US -4,278,837	7/1/1981	Best	
		US -4,747,139	5/1/1988	Taaffe	
		US -5,862,357	1/19/1999	Hagersten et al.	
		US -6,167,428	12/26/00	Ellis	
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		US -5,943,421	8/1/1999	Garbon	
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	US -				
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	US -				
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
SC		EP 0 853 279	7/1998	Nagaratna et al.		
Examiner Signature			Date Considered		6/21/07	

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Sheet	2	of	3	Attorney Docket Number	313449-P0005

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
<i>SL</i>		Geppert, L. "Solid State [Trend/Development], IEEE Spectrum, v. 33, iss. 1, 1996, pp.51-55.			
<i>SL</i>		Li, Yao, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation", LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, Nov. 16-19, 1992.			
<i>SL</i>		Dickinson et al., "An Integrated Free Space Optical Bus", 1989 IEEE International Conference on Computer Design, VLSI In Computers and Processors, p. 62-65, October 2-4, 1989.			
<i>SL</i>		Natarajan et al., "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor", Journal of Lightwave Technology, Vol. 13, No. 6, p. 1031-1040, June 6, 1995.			
<i>SL</i>		Zhao et al., "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems", Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, October 23-24, 1995.			
<i>SL</i>		Wu et al., "Microprocessor Control Signal Transmission Through Optical Fiber", Conference Record of 1992, IEEE Industry Applications Society Annual Meeting, p. 1747-1750, October 4-9, 1992.			
<i>SL</i>		Foster et al., "The Grid: Blueprint for a New Computing Infrastructure", Morgan Kaufman Publishers, Inc., 1998.			
<i>SL</i>		Hwang et al., "Scalable Parallel Computing", WCB McGraw-Hill, 1998.			
<i>SL</i>		Wilkinson, et al., "Parallel Programming", Prentice Hall, 1998.			
Examiner Signature	<i>SL</i>	Date Considered	<i>6/21/07</i>		

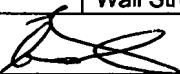
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SR		Patterson et al., "Computer Architecture: A Quantitative Approach" (2nd Edition), Morgan Kaufmann Publishers, Inc., 1996.			
SR		Culler et al., "Parallel Computer Architecture", Morgan Kaufmann Publishers, Inc., 1998.			
SR		Hennessy et al., "Computer Organization and Design", Morgan Kauffmann Publishers, Inc., 1998.			
SR		Slater, "The Microprocessor Today", IEEE Micro 1996, pp. 32-44.			
SR		Steinert-Threlkeld; "NEW BREED OF CHIP TI develops a super circuit"; The Sun Baltimore; May 4, 1992			
SR		Dallas Morning News; "LSI holds big plans for tiny chips Versatility of 'system on a chip' creates niche in microelectronics;" March 4, 1996			
SR		Mokhoff; "System-on-a-chip comes to wireless arena;" Electronic Engineering Times; Feb 12, 1996			
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SR		Ang; "System-on-a-chip to define next-generation set-top box"; Electronic Engineering Times; Dec 15, 1995			
SR		Marc; "New family of microprocessor cores from LSI Logic extends customers' system-on-a-chip design capability" Nov 7, 1994			
SR		Wall Street Journal; "Technology Brief – Advance Micro Devices Inc.: Company unveils Microchip for Hand-Held Computers"; Oct 18, 1993			
SR		Gelsinger, Patrick et al. "Microprocessors circa 2000," IEEE Spectrum, October 1989 pp. 43-47.			
SR		Yu, Albert. "The Future of Microprocessors," IEEE Micro, December 1996, pp. 46-53.			
SR		McWilliams. "Dell to Phase Out Computers Using Intel's Itanium," The Wall Street Journal, Online, September 15, 2005.			
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